IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Shiro FUJIMA

Serial No. 09/528,446

Filed March 17, 2000

GROUP 2818
Examiner S. Mai

SEMICONDUCTOR MEMORY DEVICE HAVING WORD LINES DRIVEN BY ROW SELECTING SIGNAL AND COLUMN SELECTING SIGNAL LINES ARRANGED PARALLEL TO EACH OTHER

REQUEST FOR PERMISSION TO MAKE DRAWING CORRECTIONS

RECEIVED

DEC 27 2000

TC 2800 MAIL ROOM

Commissioner for Patents

Washington, D.C. 20231

Sir:

Permission is respectfully requested to amend Figures 12-16 of the drawings in the above-identified application as shown in red on the accompanying prints.

Respectfully submitted,

YOUNG & THOMPSON

Thomas W. Perkins

Attorney for Applicant Registration No. 33,027

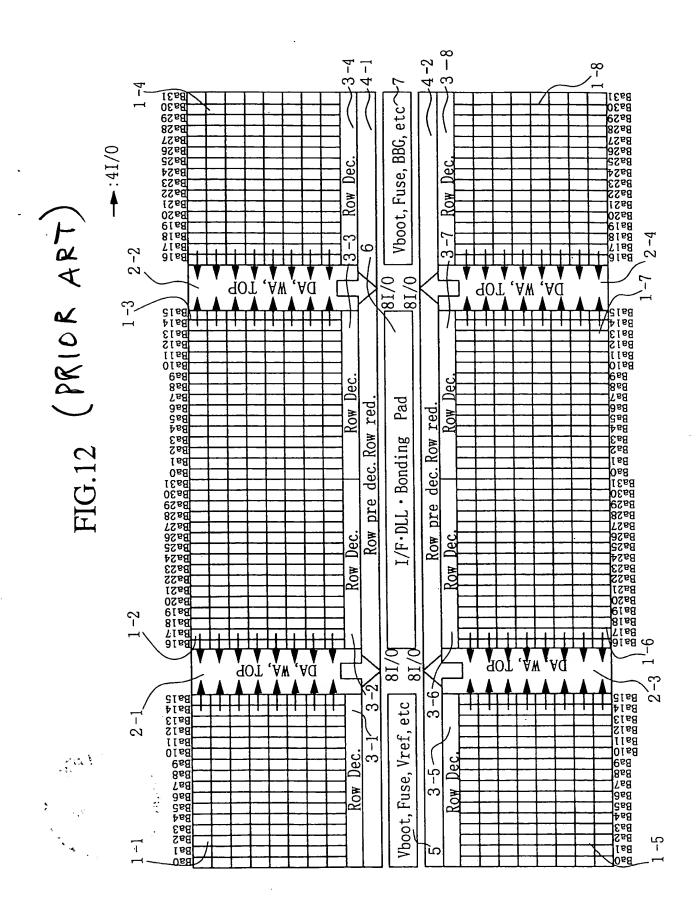
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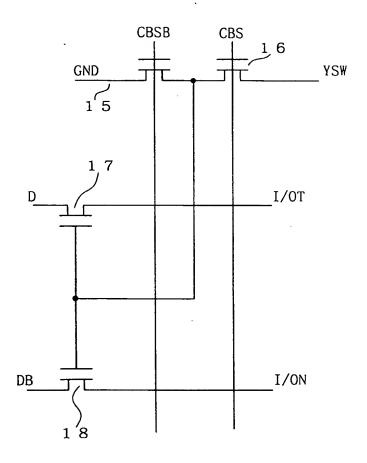
December 20, 2000

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COF DEC വ FIG.13 (PRIOR ART) 1 2 10 CBS3 CBS2 Row DEC CBS1 Bankl 0 CBS0 Bank0 ΥSW

## FIG.14 (PRIOR ART)



I/O LINE (I/OT, I/ON) DAMP  $^{\circ}$ col dec SAME WIRING LAYER ROW dec

FIG.15 (PRIOR ART)

